

IN THE CLAIMS

Kindly amend claim 1 as presented below in the listing of claims:

1. (currently amended) A microcontroller (100) comprising a command decoder (15), ~~and~~ at least one memory (14) to which the command decoder has access, the command decoder being adapted to decode at least one conditional command, such that the result of the decoding of the conditional command is dependent on the contents of said memory, a computer (12) having a direct connection (16) to said command decoder, and a common bus (13) having input (10) and output (11) connections, with said computer, said command decoder and said memory all being directly connected to said common bus.

2. (original) A microcontroller as claimed in claim 1, characterized in that the memory (14), to which the command decoder has access, is adapted in such a way that it can include input data of the microcontroller.

3. (previously presented) A microcontroller as claimed in claim 1, characterized in that the command decoder (15) is adapted in such a way that, dependent on the contents of the memory (14), to which the command decoder has access, a conditional command is

decoded as an access command for the computer (12) to different memory types.